



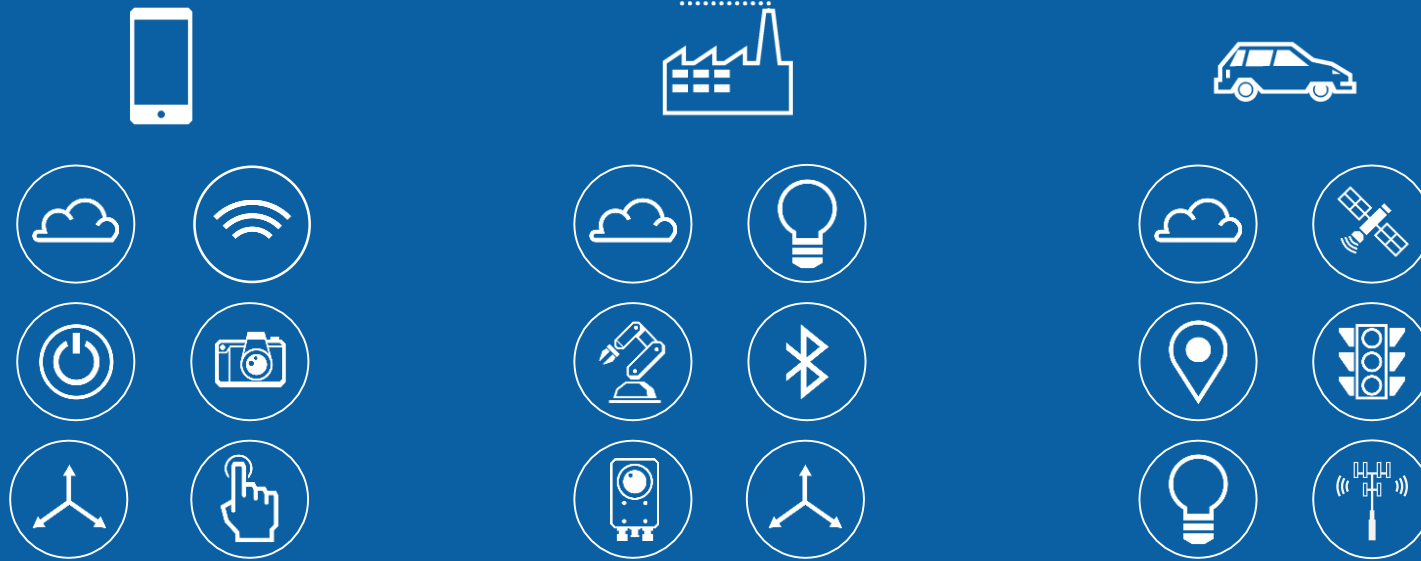
Ingo Foldvari
Business Development Manager – Academic

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ingo.foldvari@ni.com
[linkedin.com/in/ingofoldvari](https://www.linkedin.com/in/ingofoldvari)



*NI Technical Seminar
Developing Embedded DAQ, Monitoring and Control Applications*

The World of Converged Devices



More capability defined in software - Functions change rapidly - Increasingly complex to design and test

Mission Statement



NI equips engineers and scientists with systems that accelerate productivity, innovation, and discovery.

Accelerating Engineering for More Than Four Decades

1977

Introduces GPIB to connect instruments to mini computers

1986

LabVIEW starts the computer-based measurement revolution

1991

Creates the Alliance Partner Network to strengthen ecosystem

2004

Makes FPGAs accessible to engineers and scientists

2013

Introduces software-designed instrumentation

1976

NI founded

1983

Introduces first GPIB board to connect instruments to IBM PCs

1987

Releases data acquisition solutions to provide accurate measurements

1998

Creates PXI and expands opportunities with complete system solutions

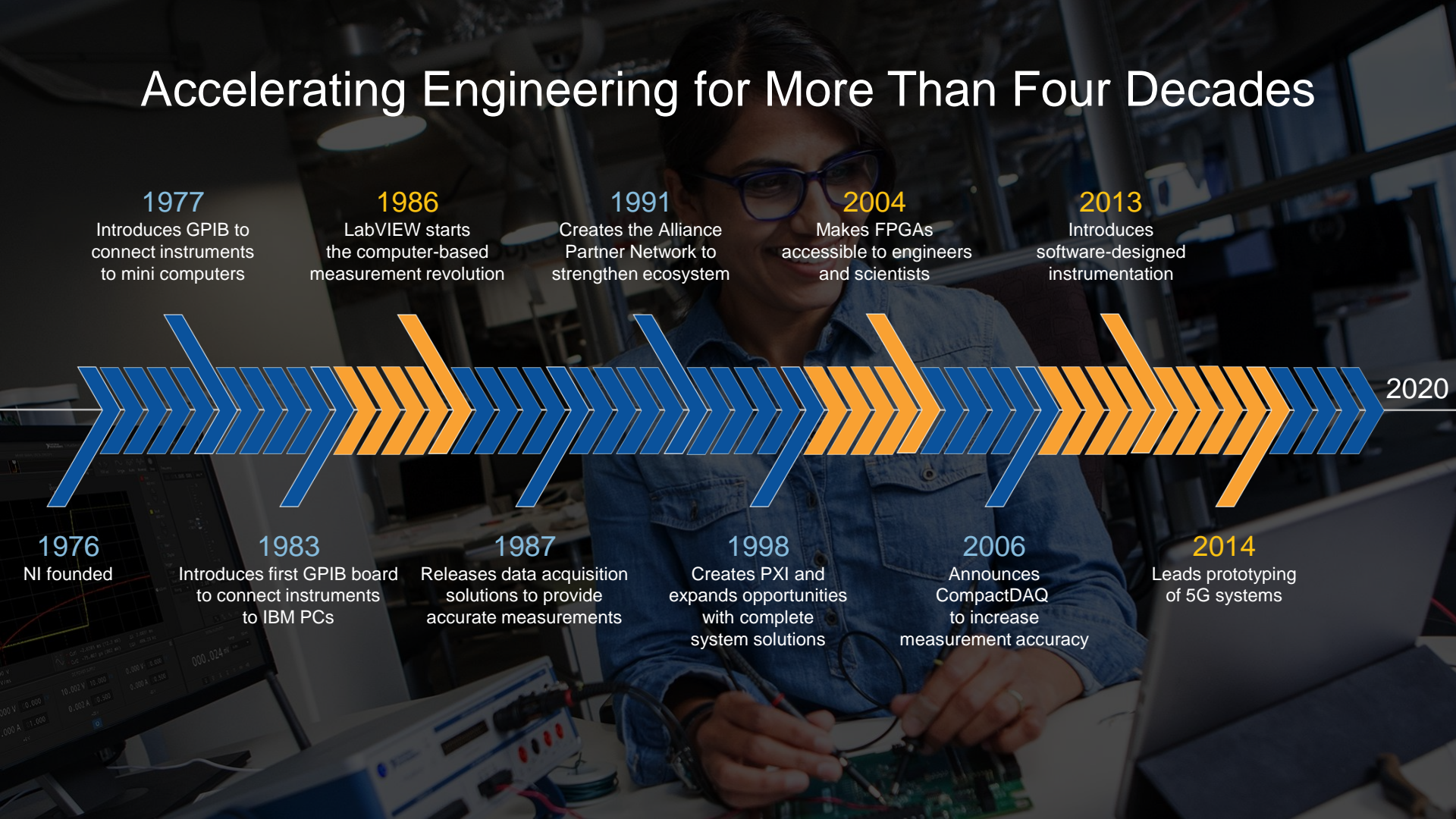
2006

Announces CompactDAQ to increase measurement accuracy

2014

Leads prototyping of 5G systems

2020





7,500+
EMPLOYEES
50+ COUNTRIES

\$1.23

BILLION
IN 2015

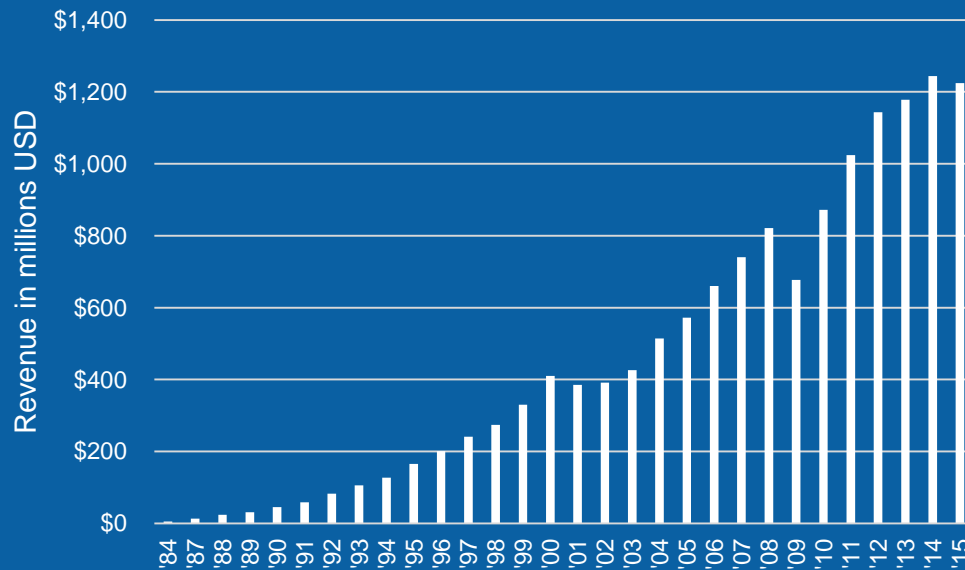


35,000+
CUSTOMERS WORLDWIDE



OVER 18%
INVESTMENT IN R&D

Long-Term Track Record of Growth



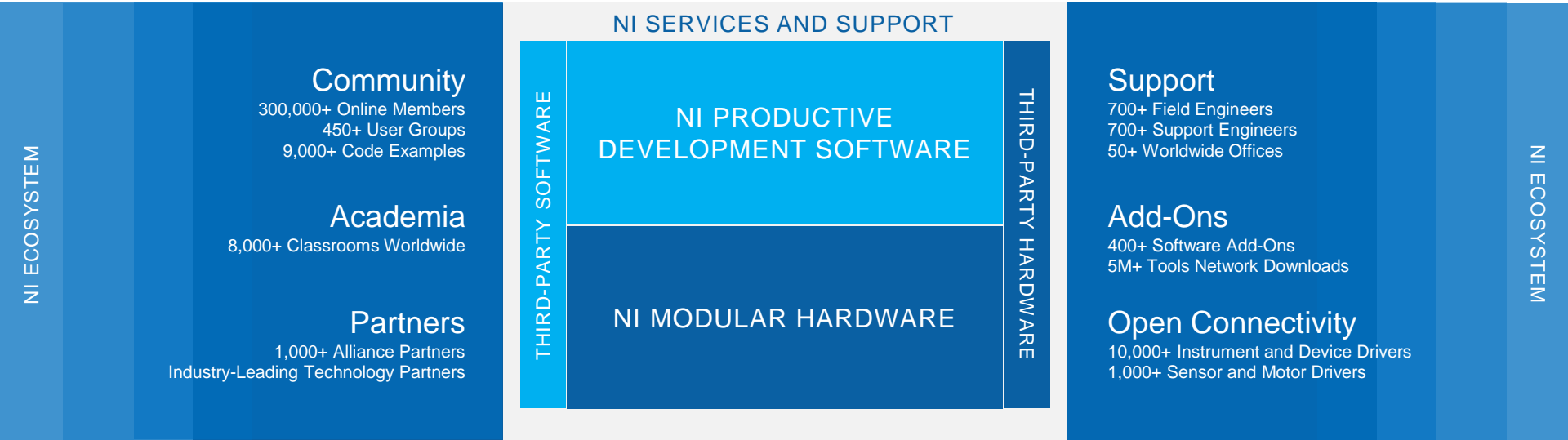
A software-centric platform approach to accelerate the development
of any system that needs test, measurement, and control.

ONE-PLATFORM APPROACH



MATLAB® is a registered trademark of The MathWorks, Inc.

ONE-PLATFORM APPROACH



Flexible Software Protects Your Investments



LabVIEW

TestStand

VeriStand

DIAdem

NI InsightCM™ Enterprise

Multisim

LabWindows™/CVI

Measurement Studio

Third-Party Software



Modular Hardware Allows You to Customize



Complete I/O Coverage
With More Than 600 Modules



Highest Data Throughput
With PCI Express



Software Extensibility
With Apps, IP, and Toolkits



Parallel Measurement Execution
With Latest Multicore Processors



Real-Time Measurements
With Timing and Synchronization



Measurement Acceleration
With User-Programmable FPGAs



Reduced Size, Power, and Weight
With Form Factor Variants



Increased Measurement Range
With Latest ADC/DAC

Our Customers' Success



Industrial Machinery



Aerospace and Defense



Electronics and Semiconductor



Academic and Research



Wireless



Transportation and Heavy Equipment



Automotive



Energy



INDUSTRIAL
MACHINERY

SIEMENS

“LabVIEW graphical system design allows us to design modular software that can be easily scaled to meet the growing requirements of rapidly evolving wind energy technology.”

—Morten Pedersen, CIM Industrial Systems A/S

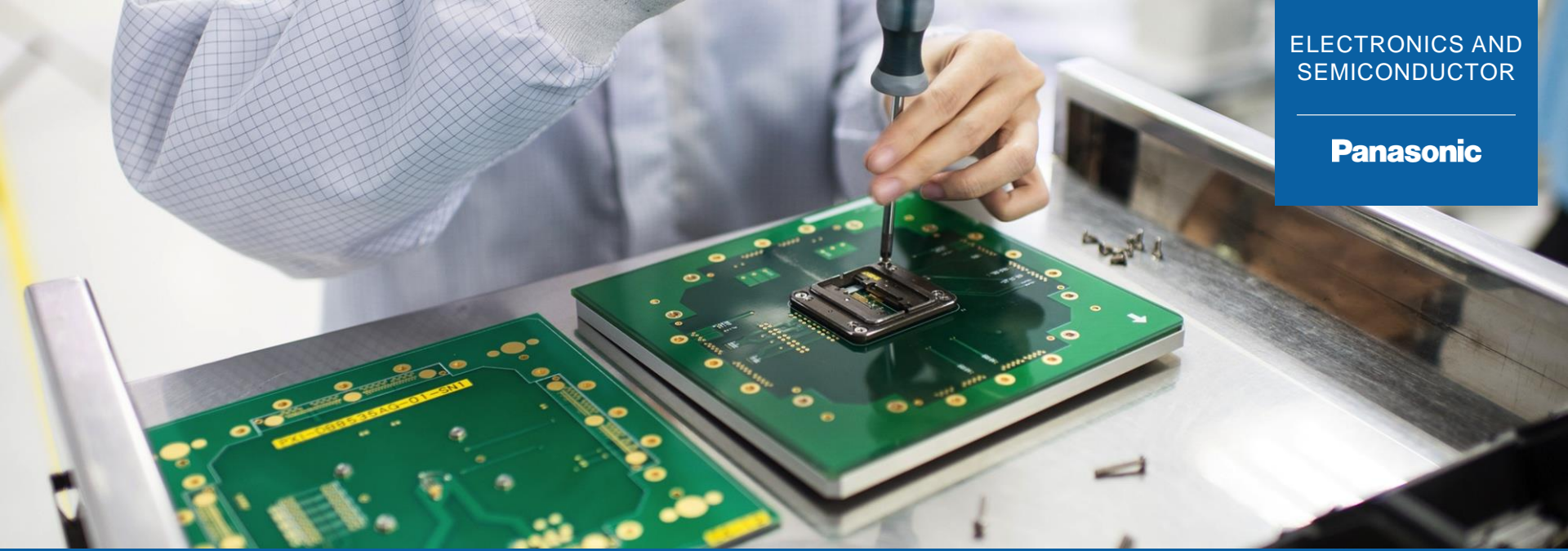
A close-up photograph of an aircraft engine, showing the fan blades and a complex arrangement of orange and yellow cables. The cables are bundled together with black zip ties and connected to various components of the engine. The lighting is dramatic, highlighting the metallic surfaces and the vibrant colors of the wiring.

AEROSPACE
AND DEFENSE



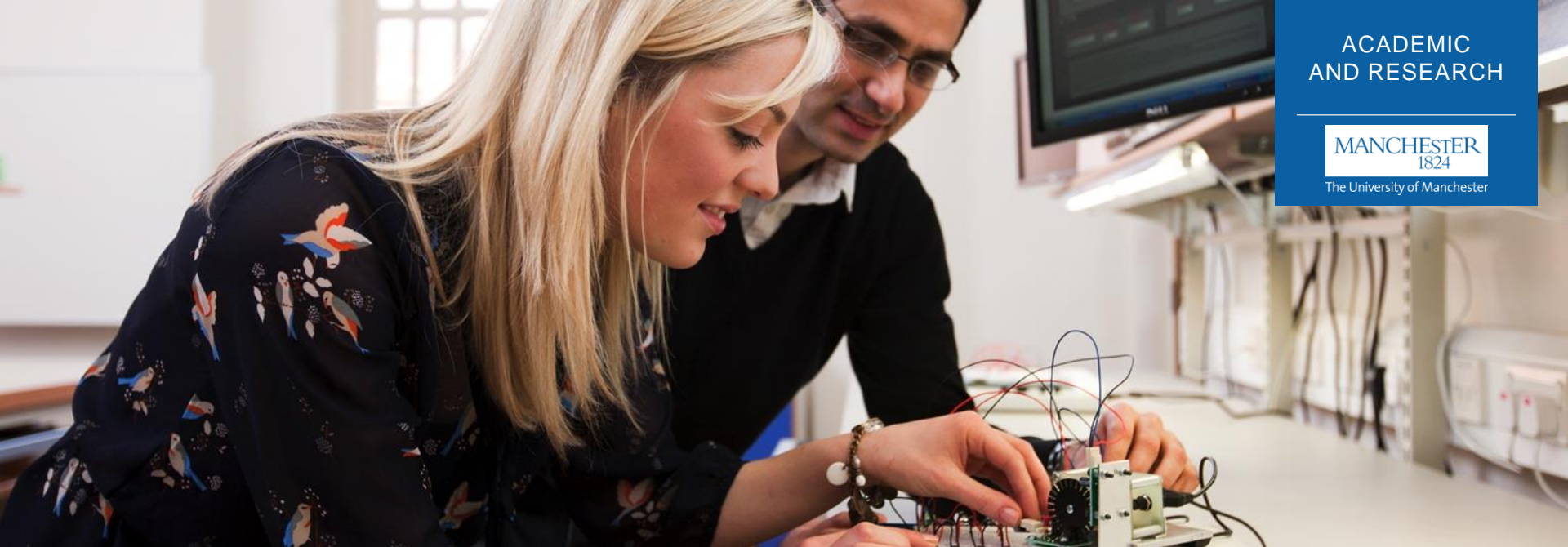
“Through the use of advanced software architecture and NI hardware, G Systems was able to provide Lockheed Martin Aeronautics with a highly configurable, expandable system to meet current and future requirements of the F-35 VSIF.”

—Michael Fortenberry, G Systems, Inc.



“The key to choosing NI products for Panasonic was the all-in-one compact enclosure, combinations of modules, the option to easily add features depending on our needs, and the ability to develop a program that has an intuitive graphical interface.”

—Takeuti Isao, Chief Engineer, Electronics Appliances



“Electronics used to seem so cryptic to me, but using NI tools in the new labs made everything so much more understandable. It’s given me the confidence to experiment with electric circuits and try out some of my own projects.”

—Joshua Elijah, Second-Year Student, The University of Manchester



WIRELESS

NOKIA

“Together, NI and Nokia Networks are reinventing the future of wireless communication and powering the fastest cell phone networks ever.”

—Lauri Oksanen, Nokia Networks

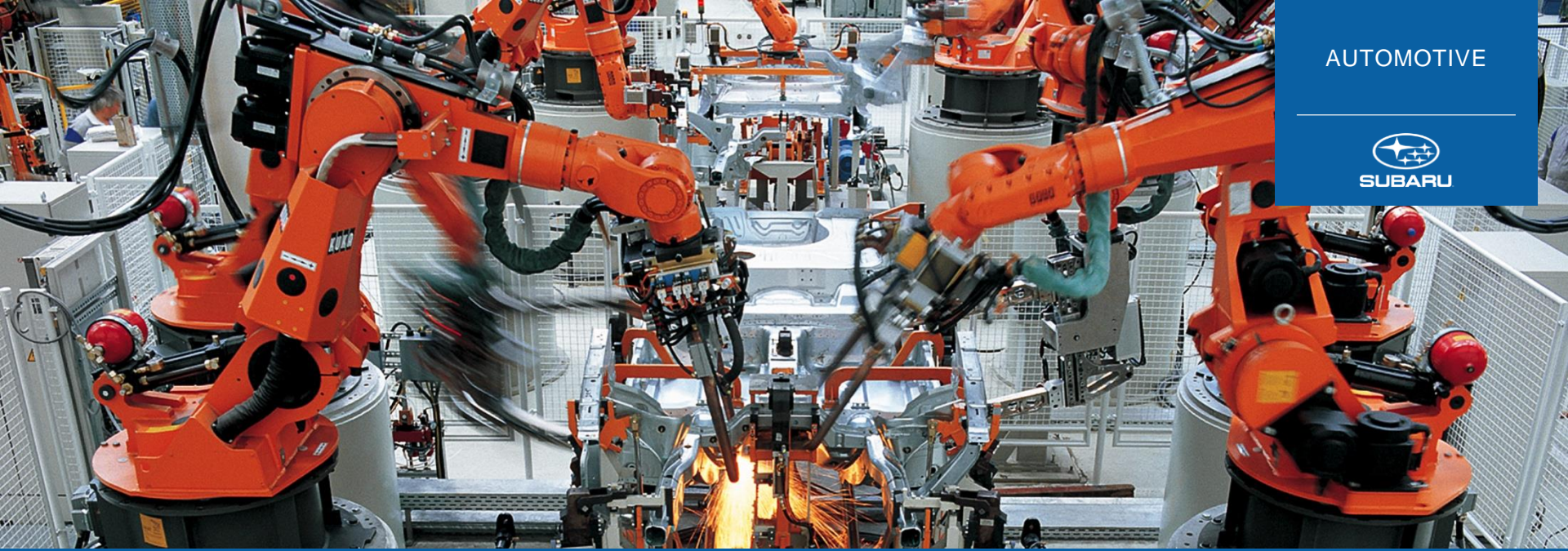


TRANSPORTATION
AND
HEAVY EQUIPMENT

FireFly
EQUIPMENT

“[The NI platform] just brings a level of control that I don’t know exists in any other platform.”

—Steven Aposhian, FireFly Equipment



AUTOMOTIVE



“By adopting FPGA-based simulation using the NI hardware and software platforms, we achieved the simulation speed and model fidelity required for verification of an electric motor ECU. We reduced test time to 1/20 of the estimated time for equivalent testing on a dynamometer.”

—Tomohiro Morita, FUJI Heavy Industries, Ltd.

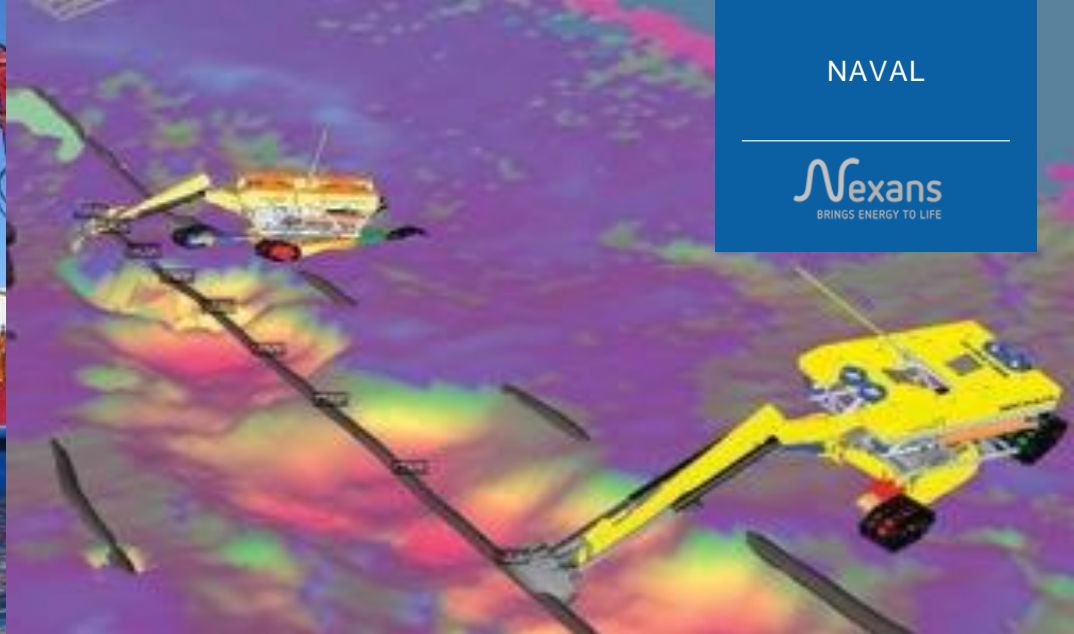


ENERGY

nationalgrid

“The high processing power of CompactRIO allows us to gather and analyze large amounts of data from anywhere on the grid as well as compile and analyze all the data to see grid-wide trends to optimize our investments to meet the energy needs of the next generation.”

—Peter Haigh, National Grid UK



NAVAL

Nexans
BRINGS ENERGY TO LIFE

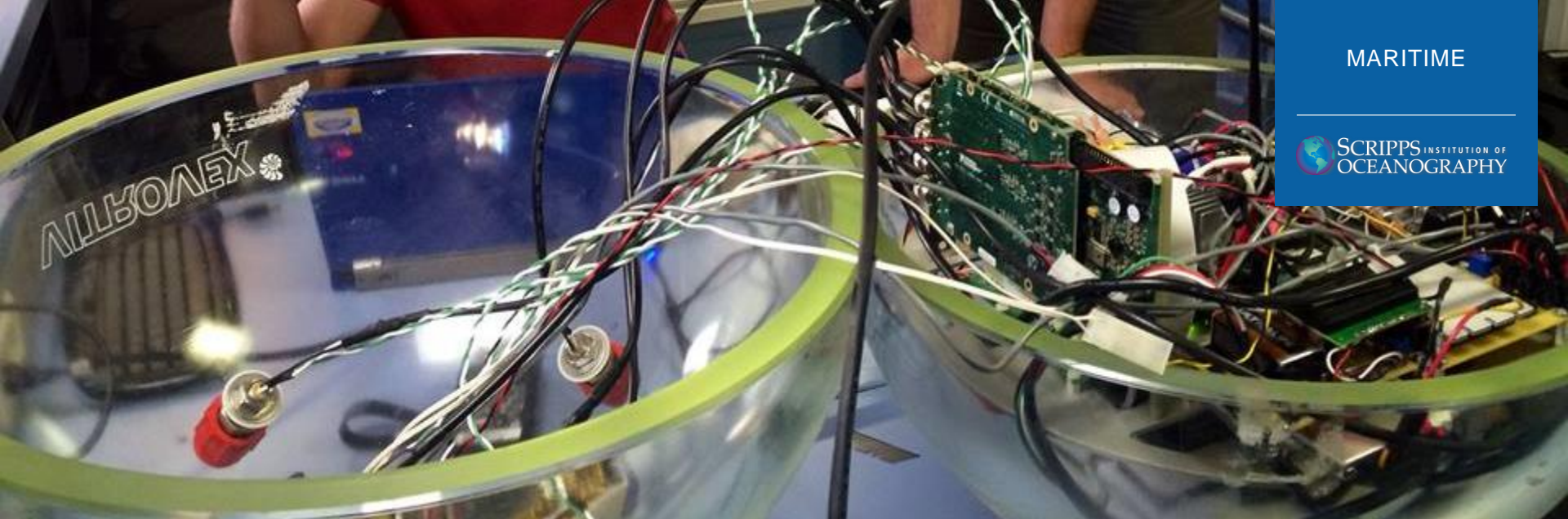
“The LabVIEW platform has helped Nexans develop a system that is easy to maintain due to the consistent programming paradigm for both HMI and embedded control, even in extreme conditions.”

—Halvor Snellingen, Nexans



“The CUBOAT project is an underwater acoustic network, that reduces size and cost by leveraging NI COTS technologies. It’s unbelievable to put something to work, see it in real life and then win an award for it.”

—Dan Ambrosio, CU Boulder



MARITIME

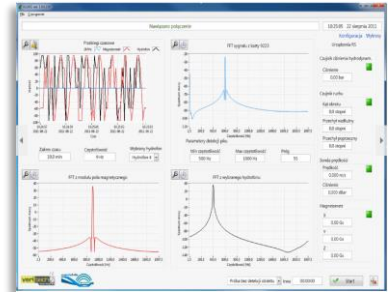
SCRIPPS INSTITUTION OF
OCEANOGRAPHY

“DeepSound is a high-bandwidth acoustic recording, free-falling system designed to profile ambient noise from the surface to depths of 12 km. DeepSound MK I, II and III are all featuring a NI PCI-4462 sound and vibration measurement device. Offering 4 simultaneously sampled analog inputs, with a sample frequency of 204.8 kHz and 114 dB dynamic range at 24 bit resolution.”

—Buckingham Lab, UCSD SIO

Additional Customer Solutions

- Measuring Underwater Radiated Noise With PXI and LabVIEW
- AUV: Underwater Acoustic Localization
- Measuring Underwater Environmental Variables With CompactRIO
- Hydrophone Calibration System
- Using LabVIEW to Automate an Underwater Drilling Mud-Pump Control System
- Using NI LabVIEW and CAN to Control an Underwater Remotely Operated Vehicle
- Smart ROVLATIS: Flexible Survey Platform for Surface and Underwater Operations
- Controlling a Remotely Operated Vehicle for Underwater Oil and Gas Operations
- Inspecting Deepwater Oil Pipelines With an Underwater Autonomous Vehicle

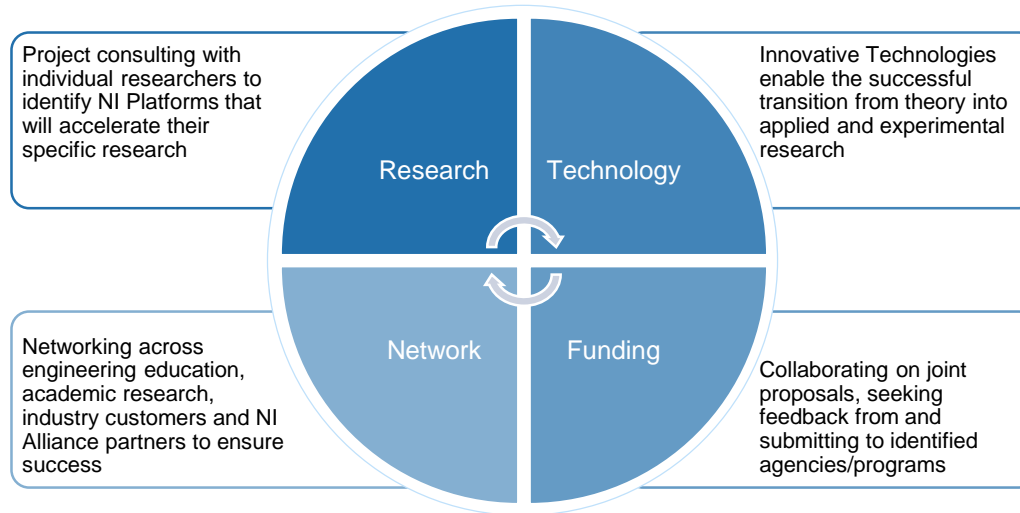




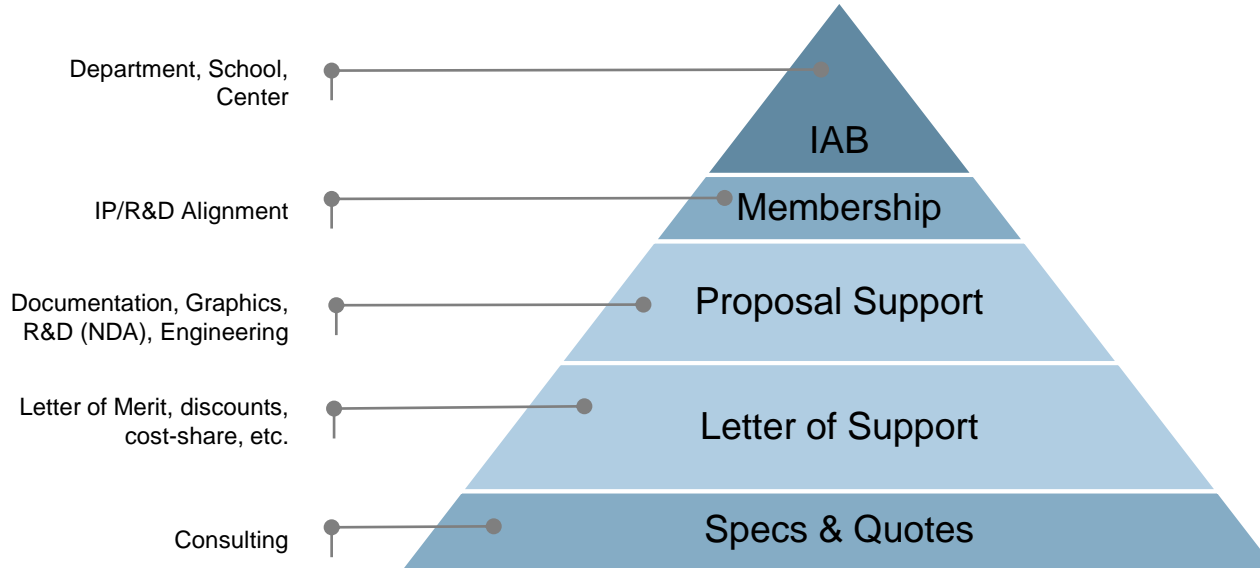
Research Collaboration

Focused to Support Academic Research

Engineers, researchers and scientist around the world rely on the NI Platform to accelerate innovation and discovery in a variety of academic research applications across a multitude of disciplines in order to advance, if not transform, the frontiers of knowledge.



Collaboration Opportunities to Apply for (more) Funding



Industry Expertise for Academic Research

Research – NI Application Thrust



ENERGY



WIRELESS



Cyber-Physical Systems



MEDICAL



Advanced Measurements

Power Conversion and Smart Grid Control

Measurement and high-speed control
Out-of-the-box experience

Wireless Prototyping / SDR

Rapid algorithm design and prototyping on FPGAs
Wide range of apps and frequency

Robotics, Automotive & Advanced Manufacturing

Full "V" Model support – smart machine design to test platform
Rapid Prototyping for Robotics and Mechatronics
Augmenting exiting control systems

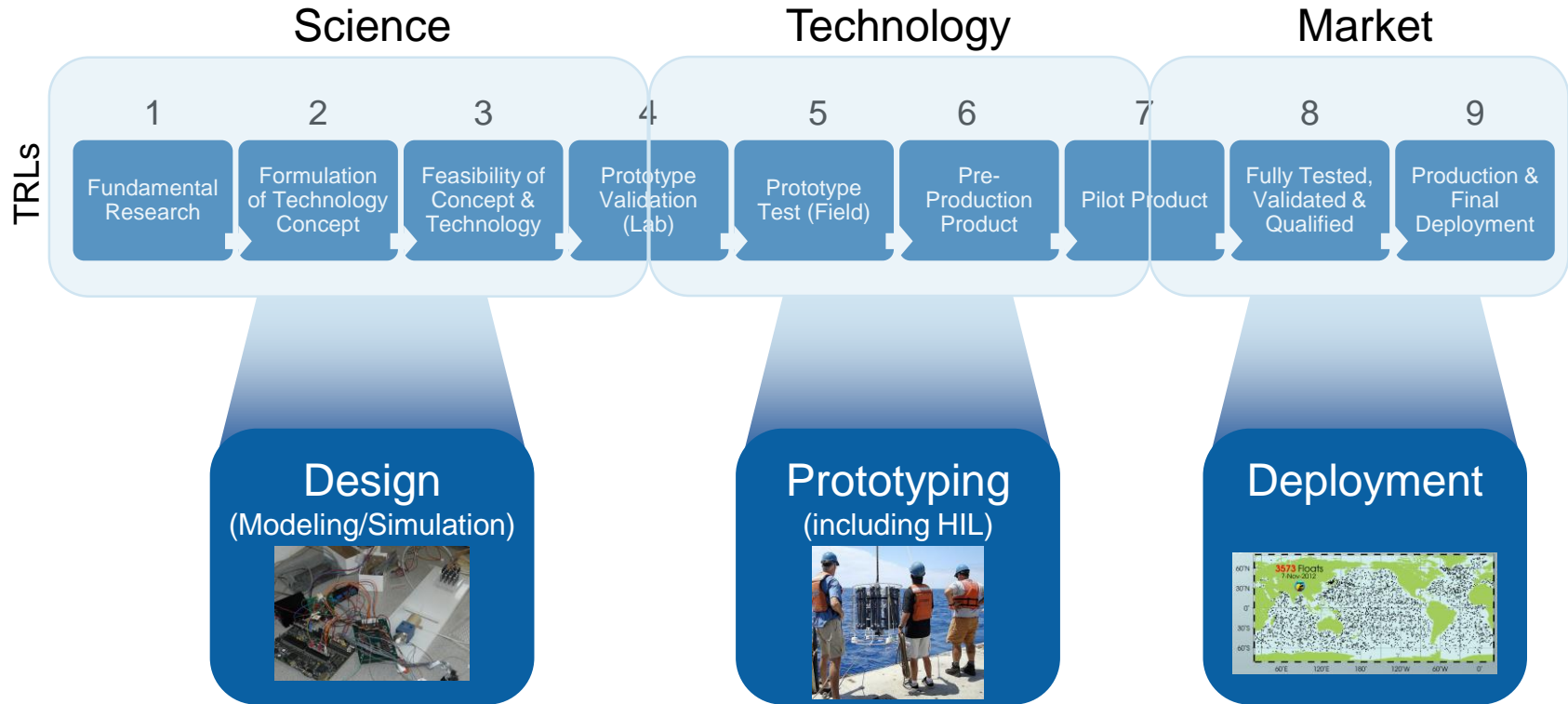
Medical & Scientific Instruments

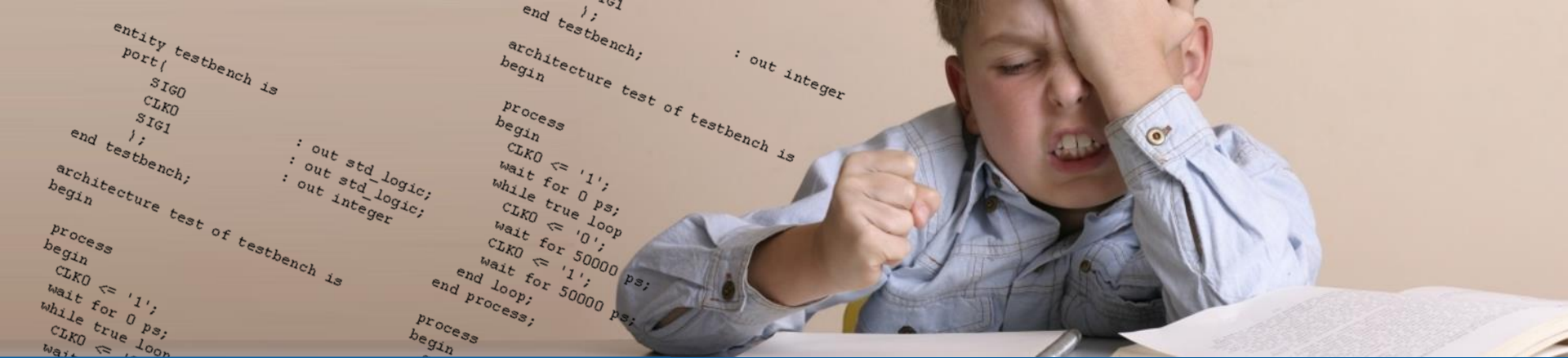
Synchronous high-speed/high-channel systems with In-line FPGA-processing
Rapid Prototyping of Medical Devices

Advance & Embedded Measurements

Any sensor
High-speed/high-channel
Sync & Trigger

Advance Academic Research through TRLs





Why go Embedded?



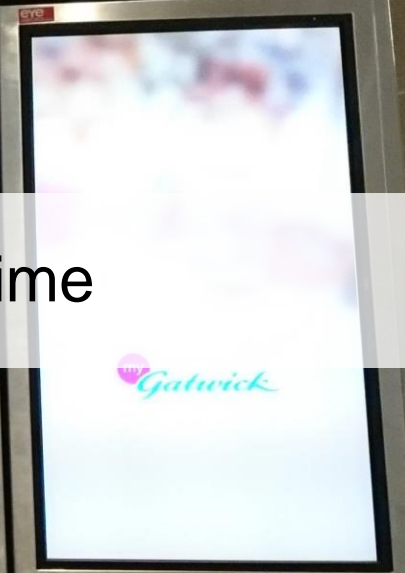
Departures

27 Jul		19:15
17:40 Naples	EZY9531	Go to Airline Info
17:55 Palma Mallorca	EZY9533	Gate closed 102
18:10 Belfast	EZY937	Gate closed 55F
18:10 Faro	EZY9829	Gate closed 111
18:25 Florence	VY8205	Delayed to 20:30
18:25 Barcelona	EZY9579	Gate closed 45G
18:30 Geneva	EZY9488	Gate closed 112
18:40 Basel	EZY9437	Gate closed 104
19:00 Barcelona	BA2710	Gate closed 564
19:00 Edinburgh	EZY913	Gate info at 19:21
19:00 Isle of Man	EZY935	Gate closed 55
19:10 Jersey	EZY949	Gate closed 4
19:20 Barcelona	BS885	Gate closes 19:18 50
19:20 Malaga	EZY9515	Gate closed 45K
19:25 Venice	BA2588	Go to Gate 560
19:30 Ibiza	EZY9601	Gate info at 19:27
19:40 Jersey	BA2778	Gate info at 19:29
19:45 Bordeaux	BA2788	Gate info at 19:45
19:45 Lyon	EZY8419	Gate closes 19:17 45J
19:45 Glasgow	EZY995	Gate closed 55B
19:45 Alicante	EZY9671	Gate closed 112
19:50 Palma Mallorca	EZY9635	Delayed to 21:50
19:55 Barcelona	EZY9561	Gate info at 19:28
20:00 Amsterdam	EZY9883	Gate info at 19:24

27 Jul		19:15
20:05 Belfast	EZY939	Gate info at 19:30
20:10 Bodrum	TOM824	Go to Gate 568
20:15 Geneva	EZ99482	Gate info at 19:45
20:25 Inverness	EZY985	Gate info at 19:36
20:25 Malaga	BA2720	Gate info at 21:19
20:55 Ibiza	BA2728	Gate info at 20:15
20:55 Edinburgh	U87474	Gate info at 20:18
20:55 Ibiza	EZY9853	Gate info at 20:25
21:05 Barcelona	VY821	Gate info at 20:15
21:10 Rome	VY8227	Gate info at 20:20
22:30 Palma Mallorca	TOM8182	Gate info at 21:50
Tuesday, July 28th 2015		
05:25 Ibiza	EZY9151	Gate info at 04:40
05:35 Pula	EZY9909	Gate info at 04:50
05:35 Ibiza	EZY9847	Gate info at 04:50
05:40 Palma Mallorca	EZY9621	Gate info at 04:55
05:45 Malaga	EZY9801	Gate info at 05:00
05:50 Sofia	EZY9873	Gate info at 05:05
05:50 Alicante	EZY9859	Gate info at 05:05
05:50 Chania	EZY9791	Gate info at 05:05
05:55 Faro	EZY9915	Gate info at 05:10
05:55 Corfu	EZY9751	Gate info at 05:10

```


An error has been detected and will prevent the screen from showing
to your computer.
SERVER_ERROR: A server error has occurred.
If this is the first time you've seen this error screen,
restart your computer. If this screen is still present, follow
these steps:
Check steps:
1. Check your internet connection.
2. Check your firewall settings.
3. Check your browser settings.
4. Check your operating system settings.
5. Check your network settings.
6. Check your hardware settings.
7. Check your software settings.
8. Check your network hardware settings.
9. Check your network software settings.
10. Check your network hardware and software settings.
If you need to use Safe Mode, please refer to the
Microsoft Support website for more information.
Technical Information:
*** STOP: 0x00000000 (0x00000000 0x00000000 0x00000000 0x00000000)
*** USBOOT - SYS - ADDRESS 0x00000000
  
```



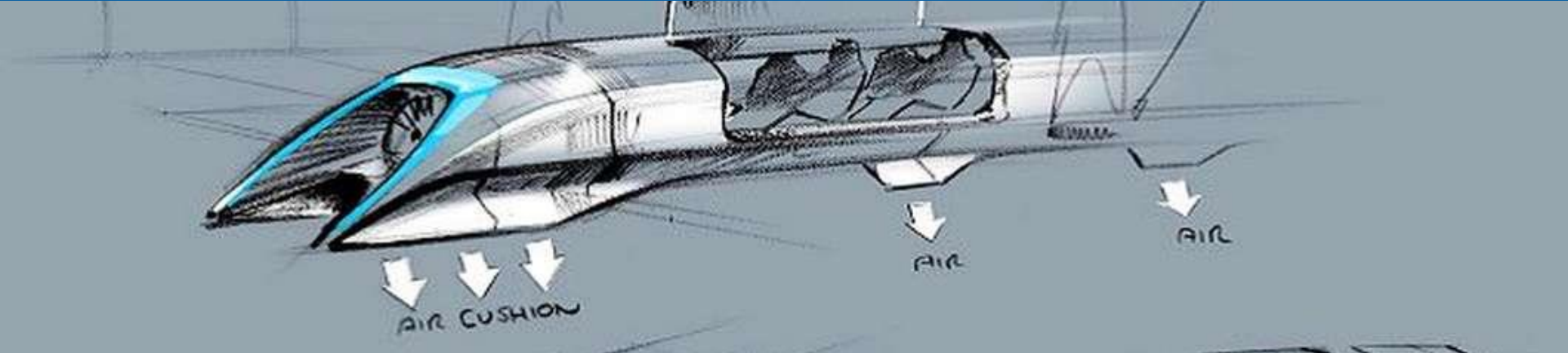
Requirement: Reliable Up-Time

A 3D rendering of a rectangular block floating above a circular hole in a surface. The block is positioned directly above the hole, and its shadow is cast onto the surface below it. The scene is set against a light gray background with a white horizontal band across the middle.

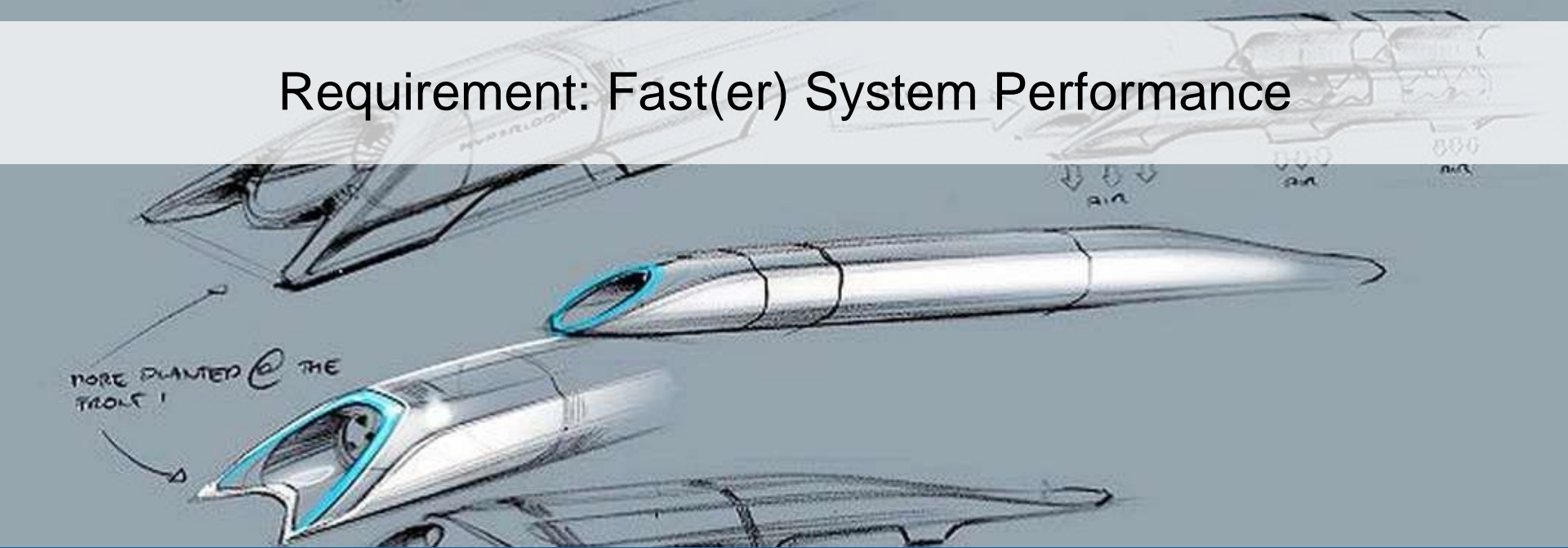
Requirement: Smaller Form Factor


An astronaut in a red and white spacesuit is sitting on a rocky surface in a desert-like environment. The astronaut is wearing a helmet and has a backpack. The background shows a bright sun setting or rising over a range of mountains, creating a warm, orange glow. The astronaut is looking towards the right side of the frame.

Requirement: Survive in Harsh Environments



Requirement: Fast(er) System Performance



A large, powerful blue wave is curling over a surfer. The surfer is a small figure in the lower right, riding the base of the wave. The water is a deep blue, and the sky is a pale, hazy blue. The wave's crest is white with foam. A semi-transparent white banner is overlaid across the middle of the image, containing the text.

Requirement: Enable Real-Time/In-Line Processing



Design Approach for Embedded Systems

Today's Engineering Challenges

Research

Budget

Resources

Proficiency

TRL Considerations

Industry Relevance

Development

Innovation

Performance

Quality

Tools

Technical Integration

Business

Profitability

Service and Support

Time to Market

Supply Chain Mgmt.

Talent/Resource Mgmt.

The Choice: Build or Buy?

Build

Advantages

- Custom HW/SW solution
- Maximum flexibility
- Ability to get exactly what you want

Disadvantages

- Long lead times for new product
- Significant resource requirements
- Higher life-cycle costs



Buy

Advantages

- Off-the-shelf HW/SW solution
- Use fewer resources because systems are pre-built
- Shorter time to market
- Lower life-cycle costs

Disadvantages

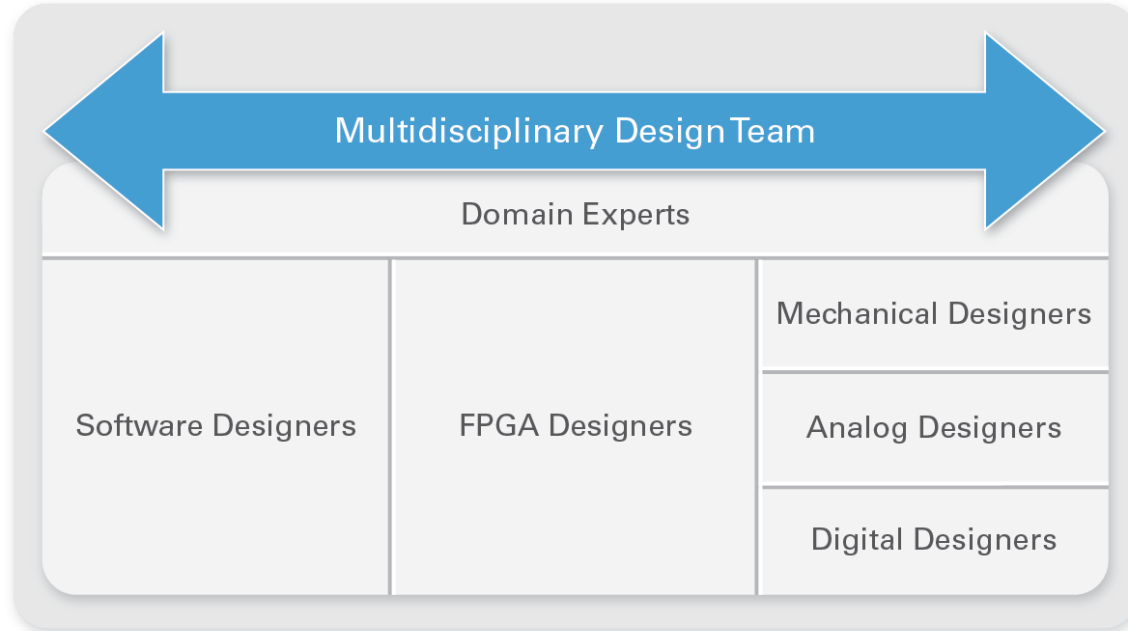
- Often pay for more than you need
- Limited flexibility (vendor defined)
- Limited functionality (vendor defined)



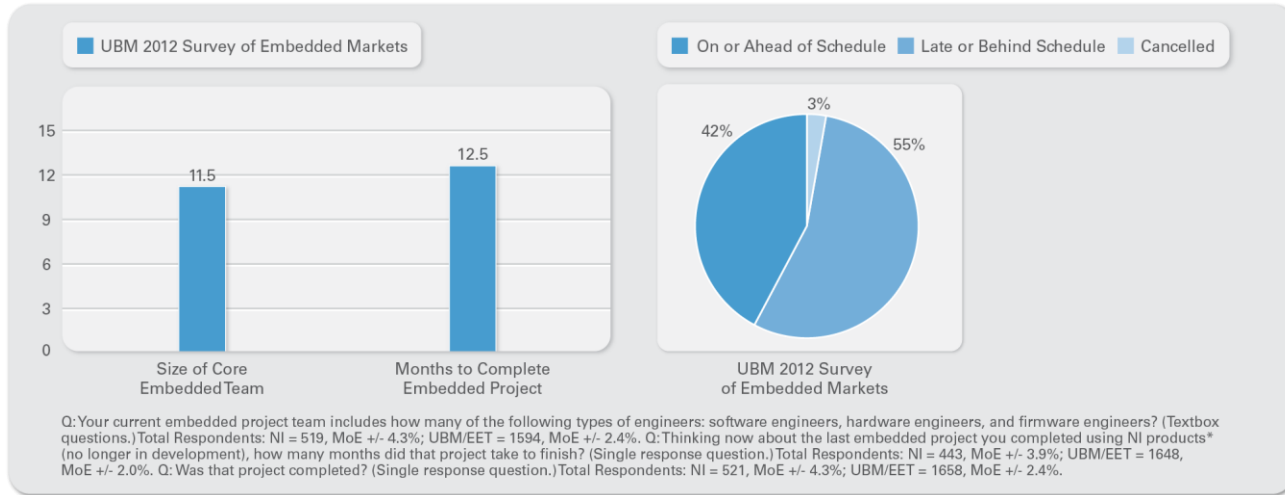
The Complexity of a Custom Build

- Hardware and software costs
- Mechanical components (non-electronic)
- EDA development tool costs
- Design specification and component selection
- Prototyping
- Hardware design
- Hardware test and verification
- Software development
- Software test and verification
- Mechanical design
- Manufacturing setup and tooling
- Manufacturing test
- Compliance and environmental engineering
- Documentation, training, and customer support
- Inventory management and EOL issues
- Sustaining engineering
- Opportunity cost

A Custom Build Requires Large Specialized Teams



The Reality of Embedded System Design



The “Buy” Option

Commercial Off-the-Shelf (COTS) Technologies

Shorter Time to Market

- Extend time in market

- Start on next project sooner

- Be more responsive to customer demands

Lower Development and Maintenance Costs

Lower Life-Cycle Management Cost

Reduced Risk Over Time

Easier Migration Paths to Future Technologies



The Dilemma: Build or Buy?

Build

Advantages

- Custom HW/SW solution
- Maximum flexibility
- Ability to get exactly what you want

Buy

Advantages

- Off-the-shelf HW/SW solution
- Use fewer resources because systems are pre-built

Build **AND** Buy!

- Higher life-cycle costs



Disadvantages

- Often pay for more than you need
- Limited flexibility (vendor defined)
- Limited functionality (vendor defined)





NI Embedded System Design

The Benefits of Off-the-Shelf Technology With the Flexibility of Custom Design

NI RIO Hardware powered by LabVIEW

NI myRIO, NI CompactRIO and NI Single-Board RIO



Student



Value



Performance

PXI & PC R Series, NI FlexRIO



High Performance

Expansion I/O



EtherCAT RIO



Ethernet RIO

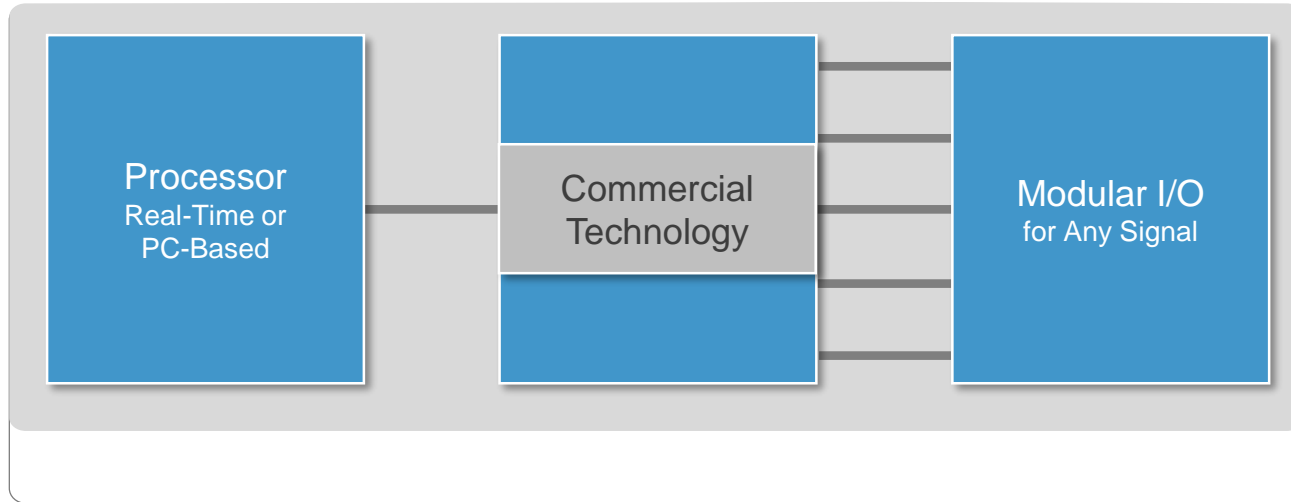


PXI

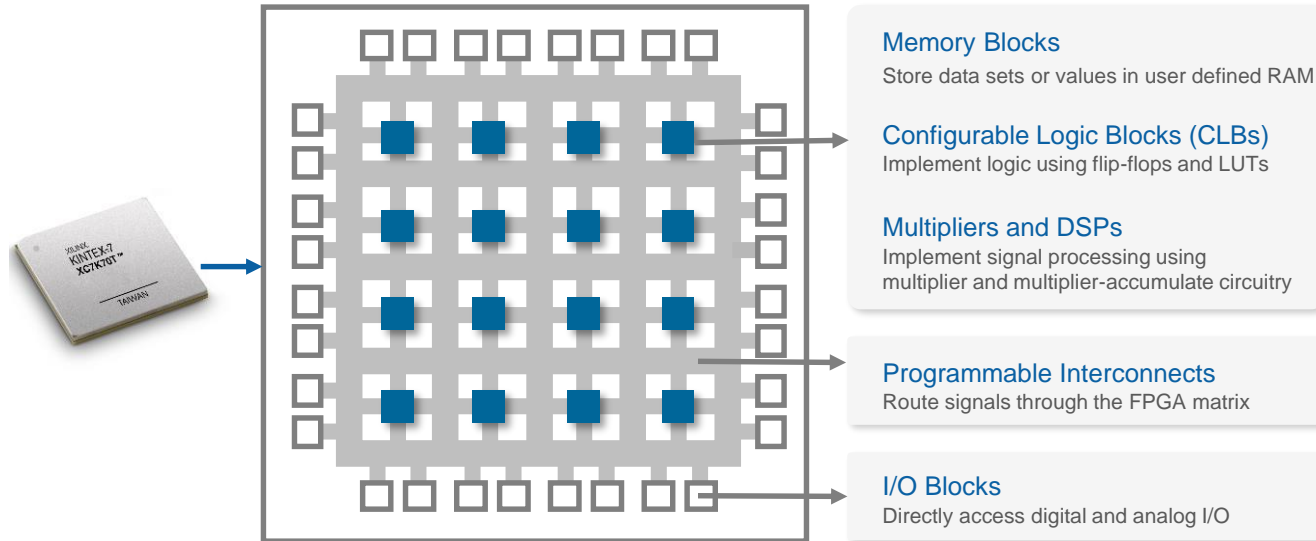


Wireless

The NI Approach to Flexible Hardware

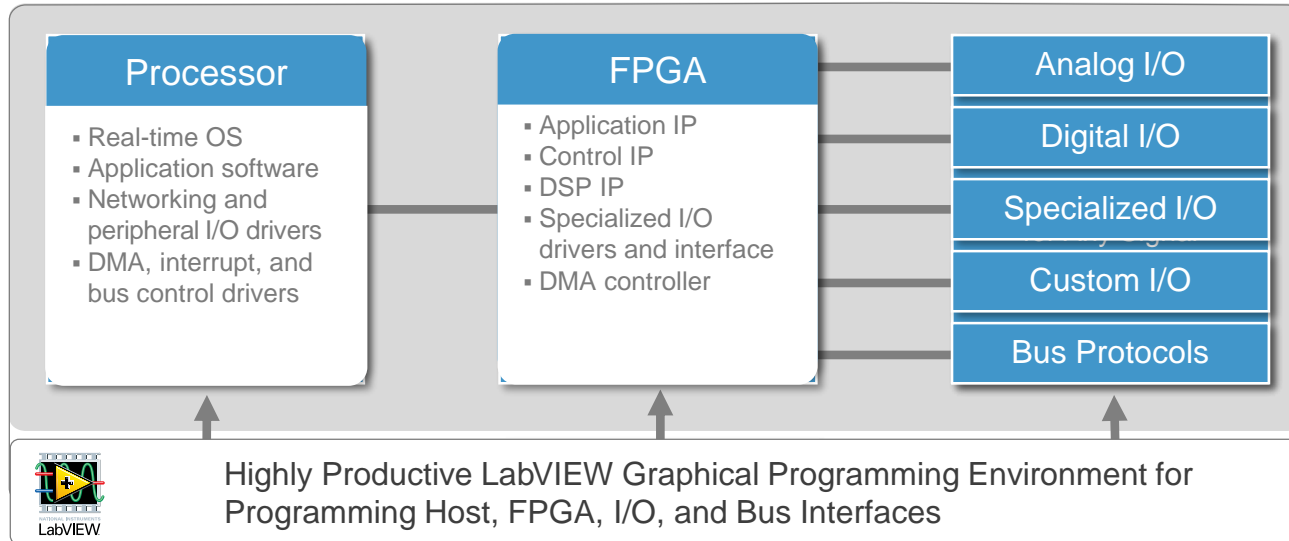


Field-Programmable Gate Array (FPGA)

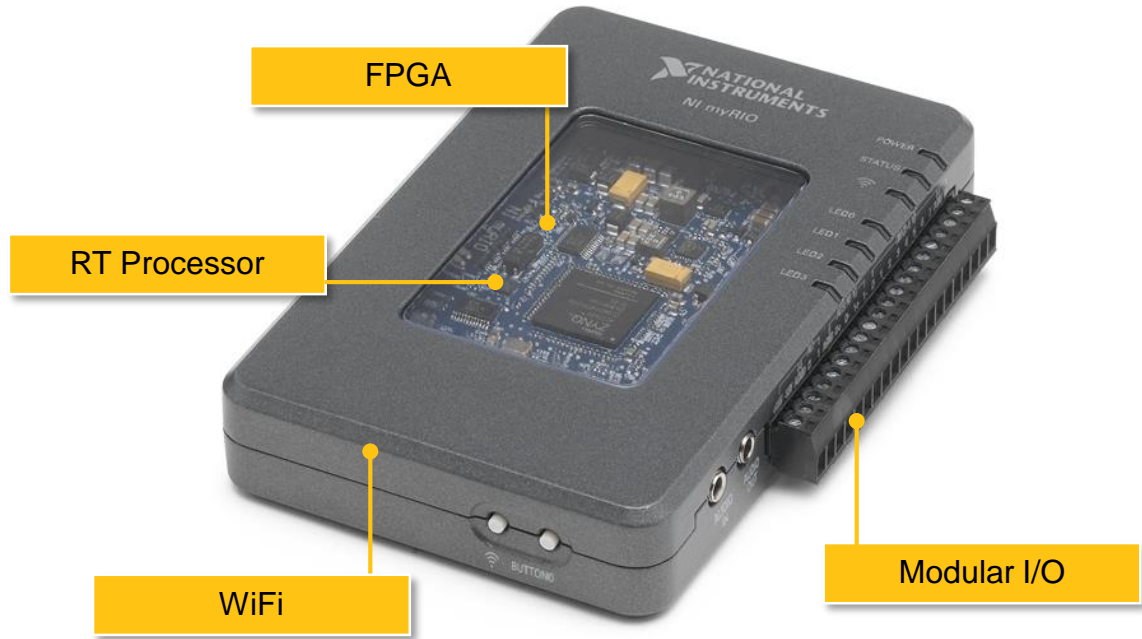


The NI Approach to Flexible Hardware

We call this the LabVIEW RIO Architecture.



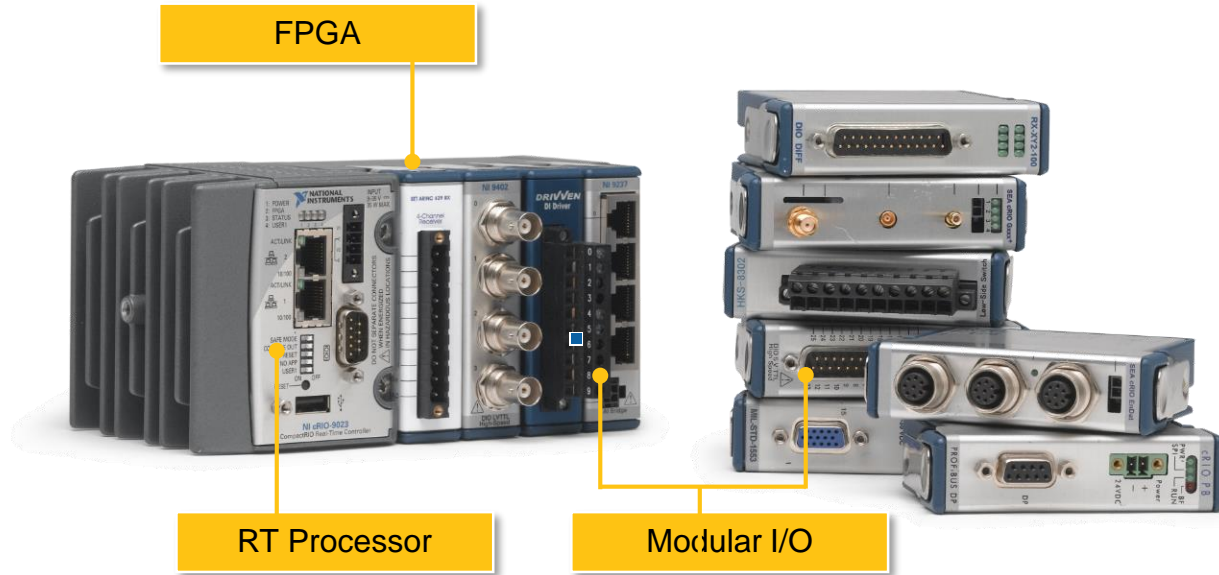
NI myRIO



Student Device
LabVIEW FPGA-based
Hands-On Engineering Education
Portable & Connected



NI CompactRIO



Extreme Ruggedness: -40 to 70 °C temperature range; 50 g shock, 5 g vibration

Real-Time Processor: Up to 1.91 GHz Quad-Core CPU

Comprehensive I/O: Analog, digital, custom, specialty, bus communication

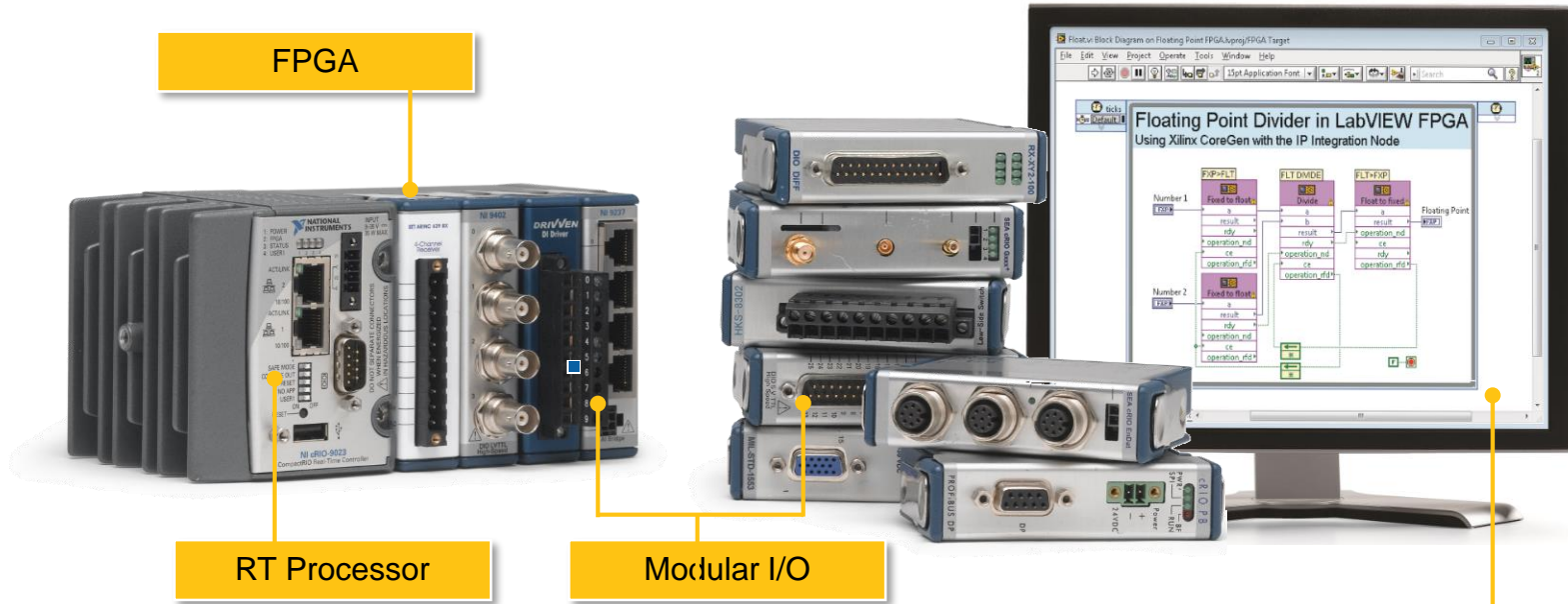
Connect to Any Sensor on Any Bus

100+ Industrial I/O Modules

- Accelerometer
- Strain gage
- Resistance
- Load cells
- Digital I/O and protocols
- Microphone
- Bus communications
- Thermocouples
- 4 to 20 mA
- Storage media
- RTD
- GPS & Sync
- Industrial vision
- Motion control

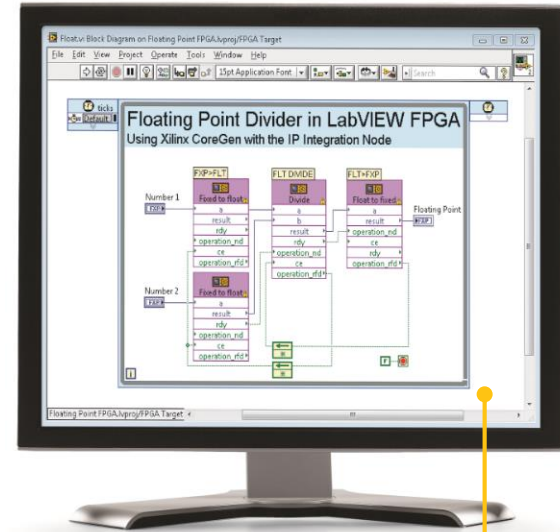
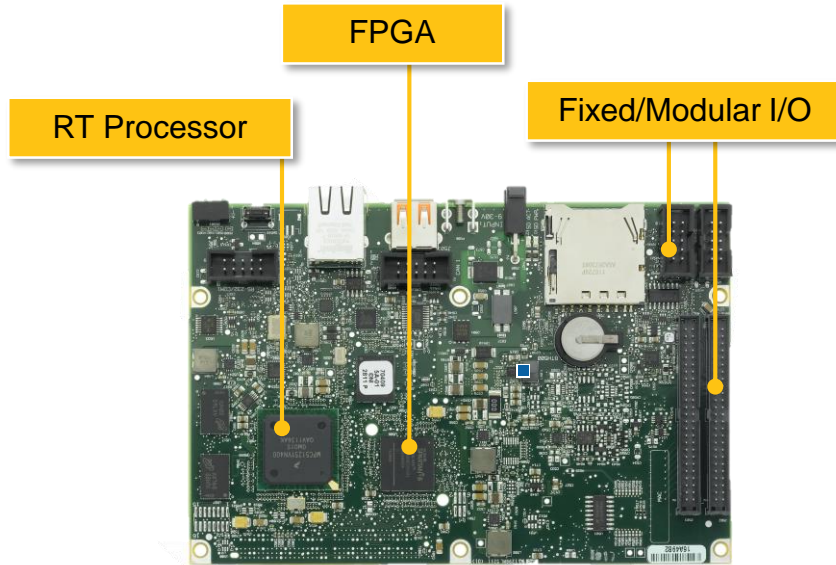


NI CompactRIO



Highly Productive LabVIEW Graphical Programming Environment for Programming Host, FPGA, I/O, and Bus Interfaces

NI Single-Board RIO



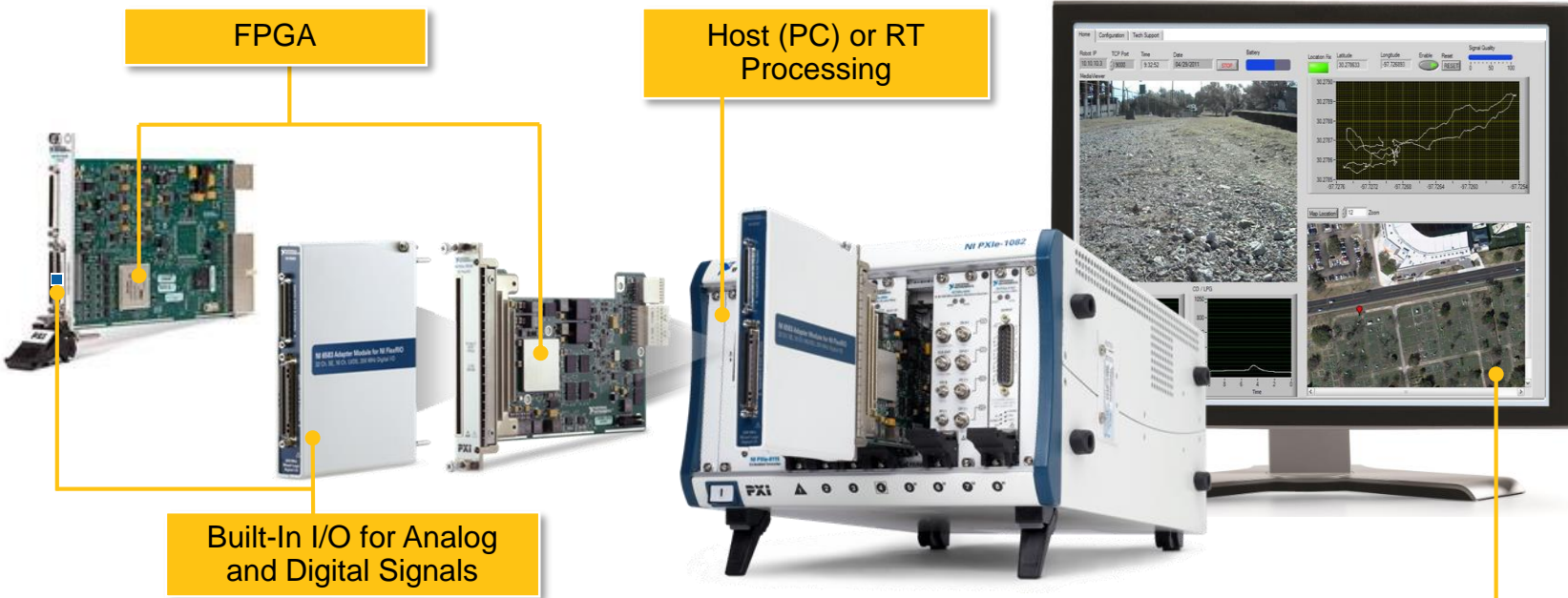
Highly Productive LabVIEW Graphical Programming Environment for Programming Host, FPGA, I/O, and Bus Interfaces

NI R Series and NI FlexRIO

FPGA

Host (PC) or RT Processing

Built-In I/O for Analog and Digital Signals



Highly Productive LabVIEW Graphical Programming Environment for Programming Host, FPGA, I/O, Bus Interfaces and GUI

ACOUSTIC

ETH Zürich

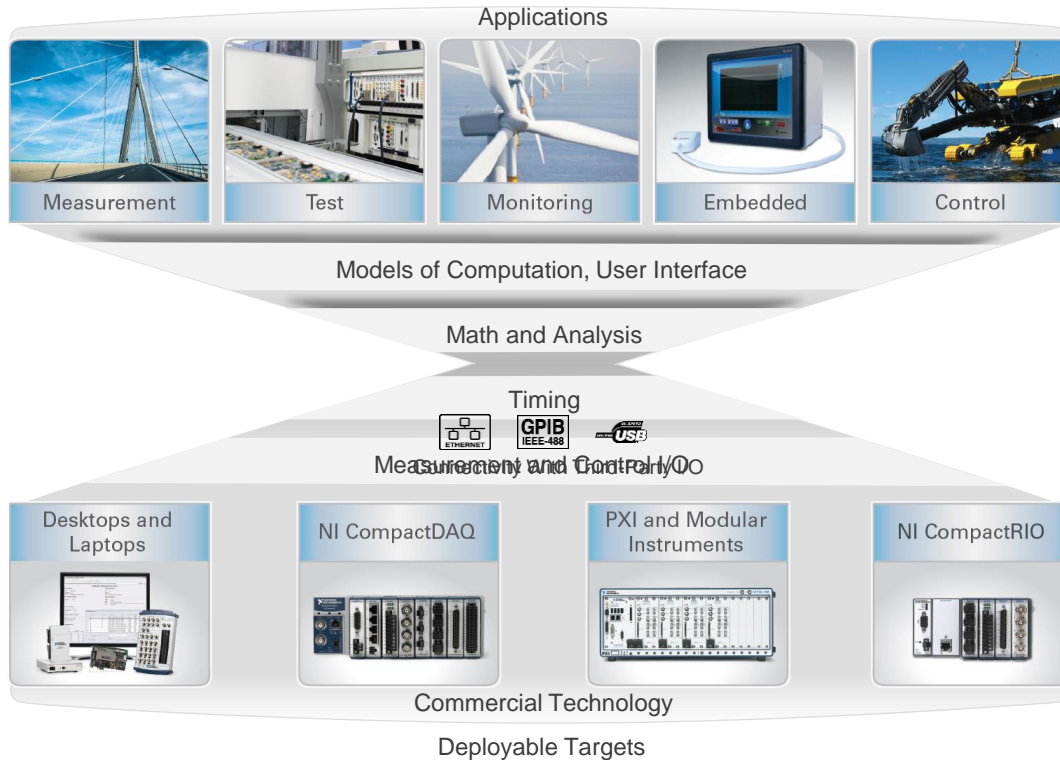


“800 sensors, 800 actuators & 800 partial differential equation solved within the $170\mu\text{sec}$. You don’t run this sort of computational load on a desktop PC. Clearly, you need a lot more computational horsepower. For this experimental setup, the researchers are using over 500 FPGAs.”

—Xilinx about ETH Zurich WaveLab,

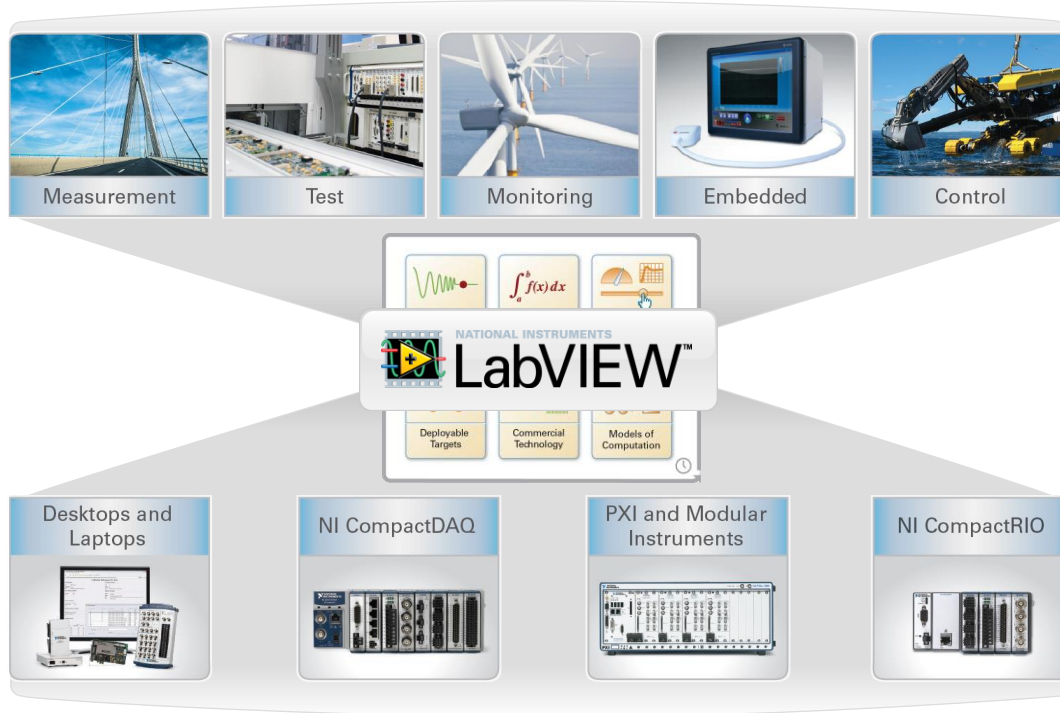
Graphical System Design

A platform-based approach for measurement, control and monitoring



Graphical System Design

A platform-based approach for measurement, control and monitoring



LabVIEW System Design Software

Project Explorer

Manage and organize all system resources, including I/O and deployment targets

Deployment Targets

Deploy LabVIEW code to the leading desktop, real-time, and FPGA hardware targets

Instant Compilation

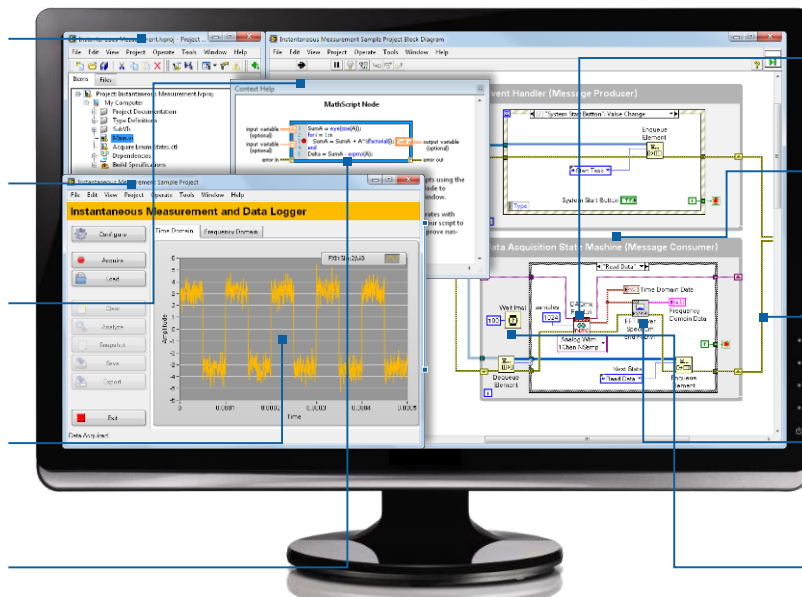
See the state of your application at all times, instantly

Front Panel

Create event-driven user interfaces to control systems and display measurements

Models of Computation

Combine and reuse .m files, C code, and HDL with graphical code



Hardware Connectivity

Bring real-world signals into LabVIEW from any I/O on any instrument

Parallel Programming

Create independent loops that automatically execute in parallel

Block Diagram

Define and customize the behavior of your system using graphical programming

Analysis Libraries

Use high-performance analysis libraries designed for engineering and science

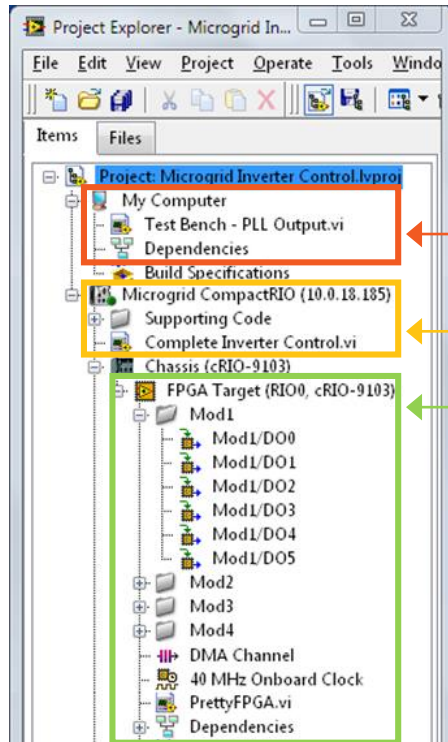
Timing

Define explicit execution order and timing with sequential data flow

Accelerates Your Research

By abstracting low-level complexity and integrating all of the tools you need to build any measurement, control or monitoring system

LabVIEW Embedded System Design Software



Windows + Desktop PC Application

Real-Time OS + Processor Application

FPGA Configuration/Application

Simplified and integrated
System Design Tool

SPOT – Self Paced Online Training

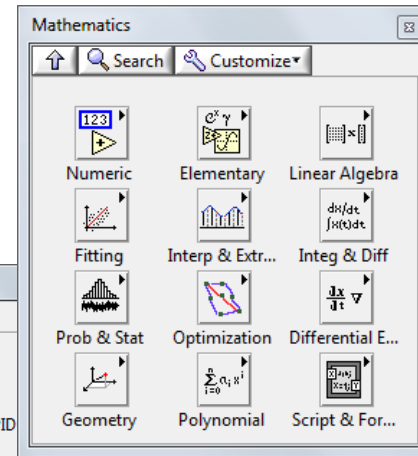
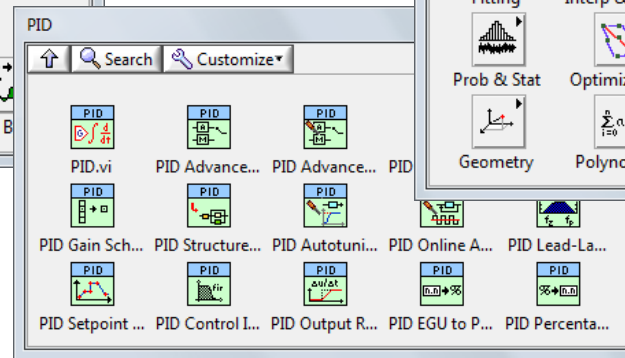
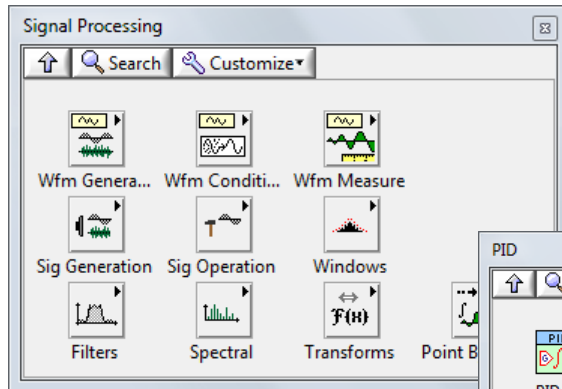
Course Title and Description	Who Should Attend	Prerequisites	
LabVIEW Core 1	New LabVIEW users and users preparing to develop applications using LabVIEW or NI Developer Suite	Experience with Microsoft Windows and writing algorithms (flowcharts, block diagrams)	Course Details
LabVIEW Core 2	New LabVIEW users and users preparing to develop applications using LabVIEW or NI Developer Suite	LabVIEW Core 1 or experience navigating and programming small applications in LabVIEW	Course Details
LabVIEW Core 3	Engineers who need to learn best practices for application and project design in LabVIEW	LabVIEW Core 1 and 2 or experience programming small to medium LabVIEW applications	Course Details
Object-Oriented Design and Programming in LabVIEW	Experienced LabVIEW users interested in using object-oriented programming architectures	LabVIEW Core 3 or experience programming medium to large LabVIEW applications	Course Details
Advanced Architectures in LabVIEW	Experienced LabVIEW users architecting medium to large applications	LabVIEW Core 3 or experience programming medium to large LabVIEW applications	Course Details
LabVIEW Real-Time 1	Developers of applications using LabVIEW Real-Time and PXI, CompactRIO, or Single Board RIO	LabVIEW Core 1 or experience navigating and programming small applications in LabVIEW	Course Details
LabVIEW Real-Time 2	LabVIEW Real-Time Module users who need to develop and deploy medium-to-large, professional real-time applications with maximum reliability and extended run times.	LabVIEW Real-Time 1 or experience developing small to medium applications in LabVIEW Real-Time	Course Details
LabVIEW FPGA	New users with applications with I/O less than 5 MHz, incorporating LabVIEW FPGA and R Series, CompactRIO, or Single-Board RIO targets.	LabVIEW Core 1 or experience navigating and programming small applications in LabVIEW	Course Details
Developing Test Programs Using TestStand	New NI TestStand users or those evaluating the software; engineers creating or maintaining test sequences	Familiarity with LabVIEW, LabWindows™/CVI, or C programming	Course Details
DIAdem Basics	New DIAdem users	Experience using Windows; programming experience is helpful but not essential	Course Details
DIAdem Advanced	New DIAdem users who have taken the DIAdem Basics course	DIAdem Basics or previous DIAdem experience	Course Details
Multisim Basics	New Multisim users and designers who need learn how to capture, simulate, and analyze circuits	Experience with Microsoft Windows and circuit design	Course Details
Ultiboard Basics	New users of NI Ultiboard and designers wanting to learn how to lay out, route, and export PCB designs	Experience with Microsoft Windows, NI Multisim, and circuit design	Course Details



LabVIEW System Development Environment

Complete
System IDE

Math and
Analysis

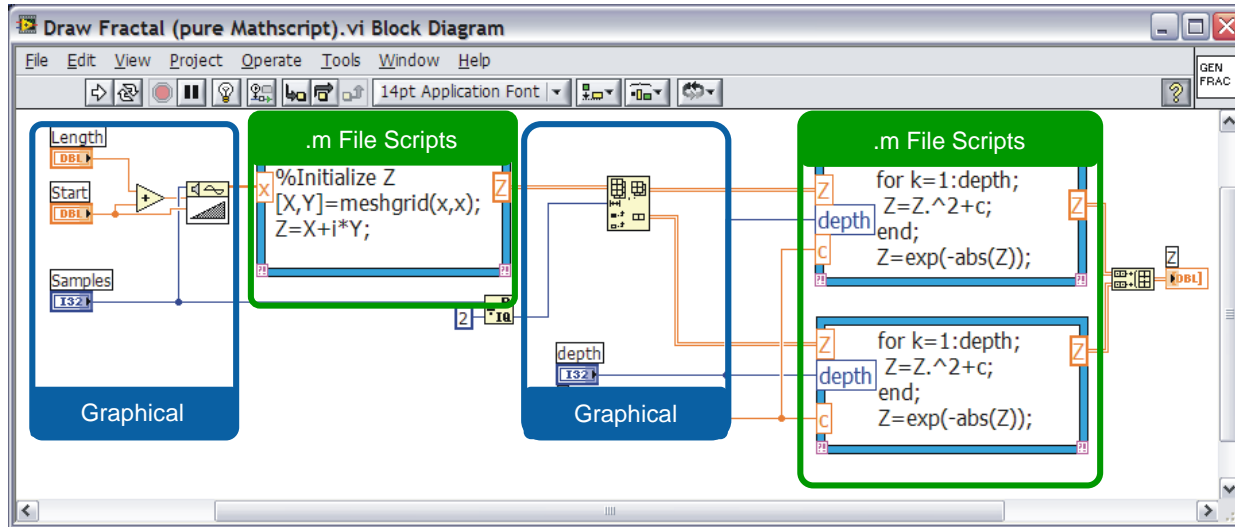


LabVIEW System Development Environment

Complete
System IDE

Math and
Analysis

Reuse of
Existing Code

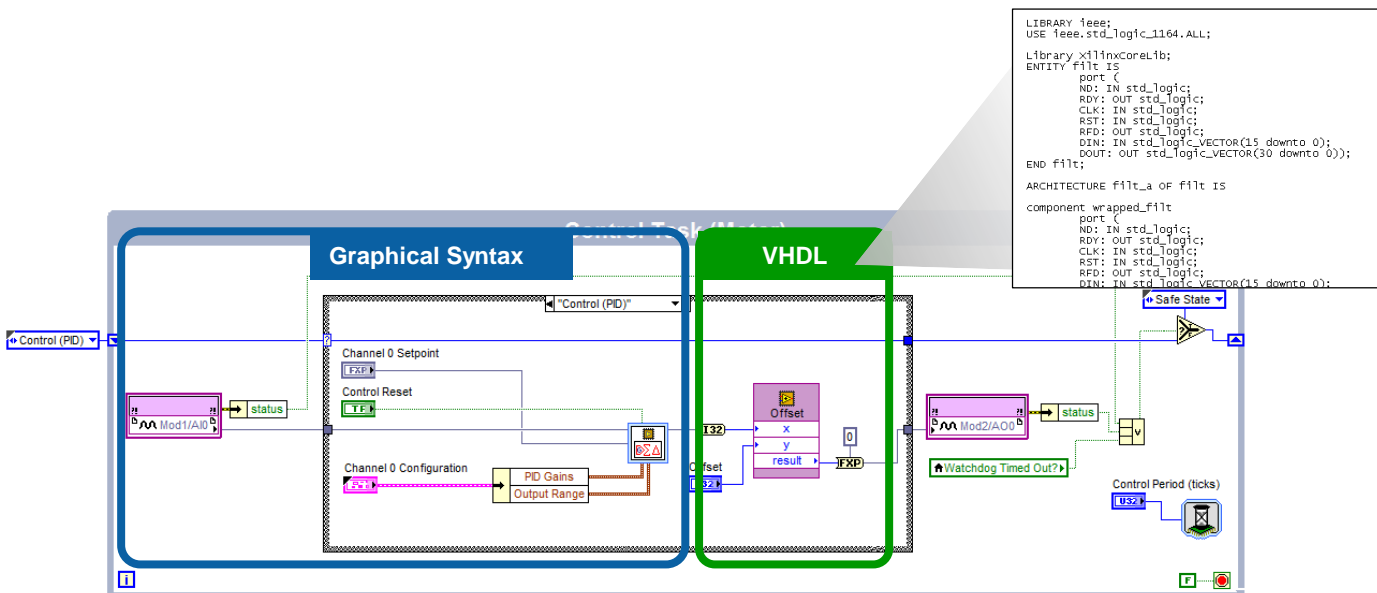


LabVIEW System Development Environment

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Reuse of
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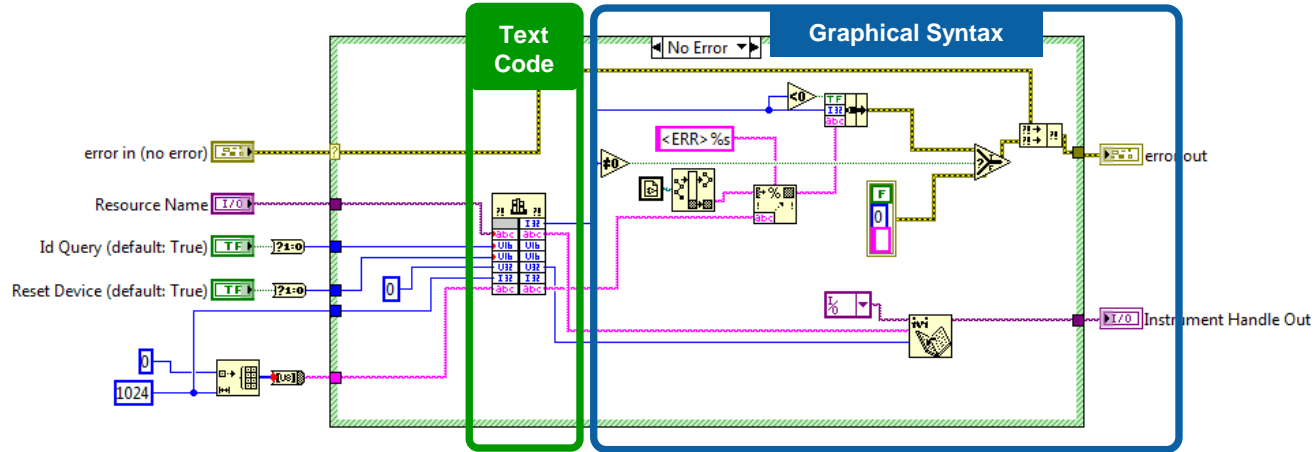


LabVIEW System Development Environment

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LabVIEW System Development Environment

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Graphical
Debugging

The screenshot displays the LabVIEW graphical programming environment for a real-time (RT) application. The main window shows the block diagram for "RT Main.vi Block Diagram on Bio Reactor.lvproj/NI-cRIO9076-0160087B". The diagram is titled "Meaurement and Control Task (pH)" and features several key components:

- A data table (array) with values:

1
0
0.01
0
- A comparison node (x) comparing the probe value to a setpoint of $0.00E+0$.
- A control logic block (y) with the following code:

```
1 if x > 0.5
2 y=true;
3 else
4 y=false;
5 end
```
- A "Probe Watch Window" overlaid on the bottom right, showing the current values of the monitored variables:

Probe(s)	Value
RT Main.vi (Bi	
[2] Probe	536.873E+6
[3] y	False

LabVIEW System Development Environment

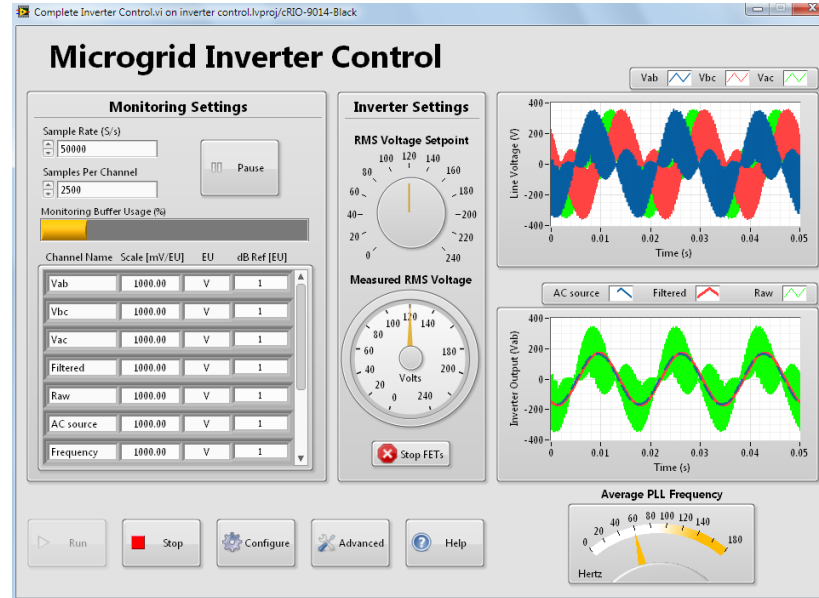
Complete
System IDE

Math and
Analysis

Reuse of
Existing Code

Graphical
Debugging

User
Interface



Live Programming: LabVIEW RT and FPGA



NI Embedded System Design

Use COTS Hardware and integrated Software Development to build customized systems that will advance academic research

Advance Research Through Integrated Hardware and Software Platform

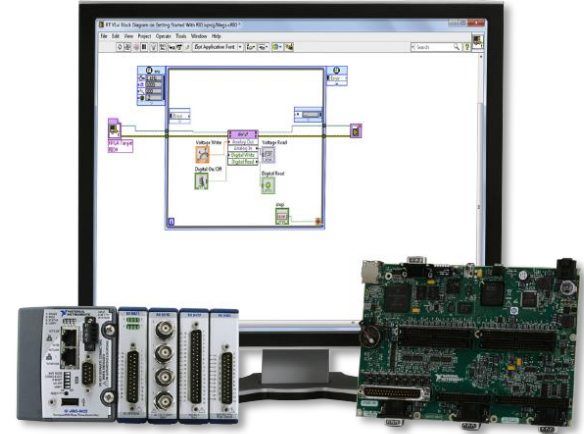
Language Designed for Measurement and Control Applications

Software that is hardware aware

- One-click deployment and execution
- Seamless, visual debugging
- Hardware timing in the LabVIEW language
- Consistent development despite CPU and FPGA variants

Abstraction of fundamental components

- Host > Real-Time > FPGA > IO
- Common I/O abstractions across many I/O modules and devices
- Scalable across NI Hardware architecture





Next steps

- Q&A
- Contact me to discuss current technical projects
- Contact me to discuss proposal collaboration



Thank you.

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